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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention METHOD AND CIRCUIT FOR TESTING A REGULATED POWER SUPPLY IN AN INTEGRATED CIRCUIT

Application Number :

Date

First Named Applicant: Joseph A. ladanza

Confirmation Number:

Attorney Docket Number: BUR920030070US1

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Decuments being submitted:	Files
Documents being submitted:	FIRES
us-assignment	BUR920030070US1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	BUR920030070assign1.tif
	BUR920030070assign2.tif
us-request	BUR920030070US1-usrequ.xml
•	us-request.dtd
	us-request.xsl
us-fee-sheet	BUR920030070US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
application-body	BUR920030070-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
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	isocyr2.ent
	isodia.ent
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	isopub.ent
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	mmlalias.ent soextblx.dtd
	SOEXTOIX.CICI BUR920030070drw1.tif
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	BUR920030070drw2.tif BUR920030070drw3.tif
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	BUR920030070drw4.tii BUR920030070drw5.tif
	BUR920030070drw5.tif
	BUR920030070drw6.til
us-declaration	BUR920030070drw7.til
Comments	5011020000700001.till
Comments	